

Abstract

The arrangement of a capacitor is automatically examined and the board layout is optimized so as to suppress the unwanted electromagnetic wave radiation and circuit malfunction. A printed circuit wiring board designing support device comprises a layout data receiving section (11) for receiving printed circuit board layout data through an input/output section, a section (12) for extracting the structures of the power supply plane and the ground plane, a via hole extracting section (13) for extracting a via hole interconnecting the wirings extending over the power supply plane and the ground plane, a capacitor extracting section (14) for extracting a capacitor connected between the power supply plane and the ground plane, a distance measuring section (15) for measuring the distance between the via hole and the capacitor, a database (3) where the allowable distance value between the via hole and the capacitor in respect to the distance between the power supply plane and the ground plane is recorded, an examination section (16) for comparing the distance between the via hole and the capacitor with the allowable distance value, and a warning section (17) for issuing a warning when the distance between the via hole and the capacitor is larger than the allowable distance value.